COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC301	Digital Signal Processing	3-1-0-4	2016

Prerequisite: EC 202 Signals & Systems

Course objectives:

1. To provide an understanding of the principles, algorithms and applications of DSP
2. To study the design techniques for digital filters
3. To give an understanding of Multi-rate Signal Processing and its applications
4. To introduce the architecture of DSP processors

Syllabus

Discrete Fourier Transform and its Properties, Linear Filtering methods based on the DFT, Frequency analysis of signals using the DFT, Computation of DFT, FFT Algorithms, IDFT computation using Radix-2 FFT Algorithms, Efficient computation of DFT of two real sequences and a 2N-Point real sequence, Design of FIR Filters, Design of linear phase FIR Filters using window methods and frequency sampling method, Design of IIR Digital Filters from Analog Filters, IIR Filter Design, Frequency Transformations, FIR Filter Structures, IIR Filter Structures, Introduction to TMS320C67xx digital signal processor, Multi-rate Digital Signal Processing, Finite word length effects in DSP systems, IIR digital filters, FFT algorithms.

Expected outcome:

The students will understand

(i) the principle of digital signal processing and applications.

(ii) the utilization of DSP to electronics engineering

Text Books:

1. Oppenheim A. V., Schafer R. W. and Buck J. R., Discrete Time Signal Processing, 3/e, Prentice Hall, 2007.
2. Proakis J. G. and Manolakis D. G., Digital Signal Processing, 4/e, Pearson Education, 2007.

References:

1. Chassaing, Rulph., DSP applications using C and the TMS320C6x DSK. Vol. 13. John Wiley & Sons, 2003.
2. Ifeachor E.C. and Jervis B. W., Digital Signal Processing: A Practical Approach, 2/e, Pearson Education, 2009.
3. Lyons, Richard G., Understanding Digital Signal Processing, 3/e. Pearson Education India, 2004.
4. Mitra S. K., Digital Signal Processing: A Computer Based Approach, 4/e McGraw Hill (India), 2014.
5. NagoorKani, Digital Signal Processing, 2e, Mc Graw –Hill Education New Delhi, 2013
6. Salivahanan, Digital Signal Processing,3e, Mc Graw –Hill Education New Delhi, 2014 (Smart book)
7. Singh A., Srinivasan S., Digital Signal Processing: Implementation Using DSP Microprocessors, Cenage Learning, 2012.

Course Plan			
Module	**Course content**	**Hours**	**End Sem. Exam Marks**
I	The Discrete Fourier Transform: DFT as a linear transformation, Relationship of the DFT to other transforms, IDFT	2	**15**
	Properties of DFT and examples Circular convolution	4	
	Linear Filtering methods based on the DFT- linear convolution using circular convolution, overlap save and overlap add methods	3	
	Frequency Analysis of Signals using the DFT	2	
II	Computation of DFT: Radix-2 Decimation in Time and Decimation in Frequency FFT Algorithms	3	**15**
	IDFT computation using Radix-2 FFT Algorithms	2	
	Efficient computation of DFT of Two Real Sequences and a 2N-Point Real Sequence	2	
FIRST INTERNAL EXAM			
III	Design of FIR Filters- Symmetric and Anti-symmetric FIR Filters	2	**15**
	Design of linear phase FIR Filters using Window methods (rectangular, Hamming and Hanning) and frequency sampling Method	6	
	Comparison of Design Methods for Linear Phase FIR Filters	1	
IV	Design of IIR Digital Filters from Analog Filters (Butterworth)	4	**15**
	IIR Filter Design by Impulse Invariance, and Bilinear Transformation	3	
	Frequency Transformations in the Analog and Digital Domain	2	
SECOND INTERNAL EXAM			
V	Block diagram and signal flow graph representations of filters	1	**20**
	FIR Filter Structures: (Linear structures), Direct Form, Cascade Form and Lattice Structure	3	
	IIR Filter Structures: Direct Form, Transposed Form, Cascade Form and Parallel Form	2	
	Computational Complexity of Digital filter structures	1	
	Computer architecture for signal processing : Introduction to TMS320C67xx digital signal processor	2	
VI	Multi-rate Digital Signal Processing: Decimation and Interpolation (Time domain and Frequency Domain Interpretation without proof)	3	**20**
	Finite word length effects in DSP systems: Introduction (analysis not required), fixed-point and floating-point DSP arithmetic, ADC quantization noise	2	

	Finite word length effects in IIR digital filters: coefficient quantization errors	2	
	Finite word length effects in FFT algorithms: Round off errors	2	
END SEMESTER EXAM			

Question Paper Pattern (End Sem Exam)

Maximum Marks: 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 40 % for theory and 60% for logical/numerical problems, derivation and proof.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC303	Applied Electromagnetic Theory	3-0-0-3	2016

Prerequisite: Nil

Course objectives:

1. To introduce basic mathematical concepts related to electromagnetic vector fields.
2. To impart knowledge on the basic concepts of electric and magnetic fields
3. To develop a solid foundation in the analysis and application of electromagnetic fields, Maxwell's equations and Poynting theorem.
4. To become familiar with propagation of signal through transmission lines and waveguides.

Syllabus:

Co-ordinate transformation, vector algebra, vector calculus, electrostatics, magneto statics, Maxwell's equations, Boundary condition, Solution of wave equation, propagation of plane EM wave in different media, Poynting vector theorem, transmission lines, Smith chart, Waveguides.

Expected outcome:

At the end of the course, students will be able:

1. To develop a solid foundation and a fresh perspective in the analysis and application of electromagnetic fields.
2. To analyse the propagation of electromagnetic waves in different media.
3. To analyze the characteristics of transmission lines.
4. To solve the different transmission line problems using Smith chart
5. To understand the different modes of propagation in waveguides.

Text Books:

1. John D. Kraus, Electromagnetics, 5/e, TMH, 2010.
2. Mathew N O Sadiku, Elements of Electromagnetics, Oxford University Press, 6/e, 2014.
3. William, H., Jf Hayt, and John A. Buck. Engineering Electromagnetics. McGraw-Hill, 8/e McGraw-Hill, 2014.

References:

1. Jordan and Balmain , Electromagnetic waves and Radiating Systems, PHI, 2/e,2013
2. Joseph A Edminister , Electromagnetics, Schaum's Outline Series McGraw Hill, 4/e, 1995
3. Martin A Plonus , Applied Electromagnetics, McGraw Hill, 2/e,1978.
4. Matthew N.O. Sadiku & S.V. Kulkarni "'Principles of Electromagnetics', Oxford University Press Inc. Sixth Edition, Asian Edition,2015
5. Nannapaneni Narayana Rao, Elements of Engineering Electromagnetics, Pearson, 6/e, 2006.
6. Umran S. Inan and Aziz S. Inan, Engineering Electromagnetics, Pearson, 2010.

Course Plan			
Module	**Course content**	**Hours**	**End Sem. Exam Marks**
I	Review of vector calculus, Spherical and Cylindrical coordinate system, Coordinate transformation	1	**0**
	Curl, Divergence, Gradient in spherical and cylindrical coordinate system.	1	
	Electric field – Application of Coulomb's law, Gauss law and Amperes current law (proof not required, simple problems only)	1	**15**
	Poisson and Laplace equations (proof not required, simple problems only), Determination of E and V using Laplace equation.	1	
	Derivation of capacitance and inductance of two wire transmission line and coaxial cable. Energy stored in Electric and Magnetic field.	2	
	Displacement current density, continuity equation. Magnetic vector potential. Relation between scalar potential and vector potential.	2	
II	Maxwell's equation from fundamental laws.	1	**15**
	Boundary condition of electric field and magnetic field from Maxwell's equations	1	
	Solution of wave equation	1	
	Propagation of plane EM wave in perfect dielectric, lossy medium, good conductor, media-attenuation, phase velocity, group velocity, skin depth.	3	
FIRST INTERNAL EXAM			
III	Reflection and refraction of plane electromagnetic waves at boundaries for normal & oblique incidence (parallel and perpendicular polarization), Snell's law of refraction, Brewster angle.	4	**15**
	Power density of EM wave, Poynting vector theorem, Complex Poynting vector.	3	
	Polarization of electromagnetic wave-linear, circular and elliptical polarisation.	2	
IV	Uniform lossless transmission line - line parameters	1	**15**
	Transmission line equations, Voltage and Current distribution of a line terminated with load	2	
	Reflection coefficient and VSWR. Derivation of input impedance of transmission line.	2	
SECOND INTERNAL EXAM			
V	Transmission line as circuit elements (L and C).	2	**20**
	Half wave and quarter wave transmission lines.	1	
	Development of Smith chart - calculation of line impedance and VSWR using smith chart.	2	

	Single stub matching (Smith chart and analytical method).	2	
VI	Parallel-Plate Waveguide - TE & TM waves.	1	**20**
	The hollow rectangular wave guide – modes of propagation of wave- dominant mode, group velocity and phase velocity - derivation and simple problems only.	3	
	Attenuation in wave guides, guide wavelength and impedance -derivation and simple problems only.	3	
END SEMESTER EXAM			

Question Paper (End semester exam)

Maximum marks : 100 **Time: 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 50 % for theory and 50% for logical/numerical problems, derivation and proof.

Course code	Course Name	L-T-P - Credits	Year of Introduction
HS300	Principles of Management	3-0-0-3	2016

Prerequisite : Nil

Course Objectives

- To develop ability to critically analyse and evaluate a variety of management practices in the contemporary context;
- To understand and apply a variety of management and organisational theories in practice;
- To be able to mirror existing practices or to generate their own innovative management competencies, required for today's complex and global workplace;
- To be able to critically reflect on ethical theories and social responsibility ideologies to create sustainable organisations.

Syllabus

Definition, roles and functions of a manager, management and its science and art perspectives, management challenges and the concepts like, competitive advantage, entrepreneurship and innovation. Early contributors and their contributions to the field of management. Corporate Social Responsibility. Planning, Organizing, Staffing and HRD functions, Leading and Controlling. Decision making under certainty, uncertainty and risk, creative process and innovation involved in decision making.

Expected outcome.

A student who has undergone this course would be able to

i. manage people and organisations
ii. critically analyse and evaluate management theories and practices
iii. plan and make decisions for organisations
iv. do staffing and related HRD functions

Text Book:

Harold Koontz and Heinz Weihrich, *Essentials of Management*, McGraw Hill Companies, 10th Edition.

References:

1. Daft, *New era Management*, 11th Edition, Cengage Learning
2. Griffin, *Management Principles and Applications*, 10th Edition, Cengage Learning
3. Heinz Weirich, Mark V Cannice and Harold Koontz, *Management: a Global, Innovative and Entrepreneurial Perspective*, McGraw Hill Education, 14th Edition
4. Peter F Drucker, *The Practice of Management*, McGraw Hill, New York
5. Robbins and Coulter, *Management*, 13th Edition, 2016, Pearson Education

Course Plan

Module	Contents	Hours	Sem. Exam Marks
I	Introduction to Management: definitions, managerial roles and functions; Science or Art perspectives- External environment- global, innovative and entrepreneurial perspectives of Management (3 Hrs.)– Managing people and organizations in the context of New Era- Managing for competitive advantage - the Challenges of Management (3 Hrs.)	6	15%

II	**Early Contributions and Ethics in Management**: Scientific Management- contributions of Taylor, Gilbreths, Human Relations approach-contributions of Mayo, McGregor's Theory, Ouchi's Theory Z (3 Hrs.) Systems Approach, the Contingency Approach, the Mckinsey 7-S Framework Corporate Social responsibility- Managerial Ethics. (3 Hrs)	6	15%
FIRST INTERNAL EXAMINATION			
III	**Planning:** Nature and importance of planning, -types of plans (3 Hrs.)- Steps in planning, Levels of planning - The Planning Process. – MBO (3 Hrs.).	6	15%
IV	**Organising for decision making:** Nature of organizing, organization levels and span of control in management Organisational design and structure –departmentation, line and staff concepts (3 Hrs.) Limitations of decision making- Evaluation and selecting from alternatives- programmed and non programmed decisions - decision under certainty, uncertainty and risk-creative process and innovation (3 Hrs.)	6	15%
SECOND INTERNAL EXAMINATION			
V	**Staffing and related HRD Functions:** definition, Empowerment, staff – delegation, decentralization and recentralisation of authority – Effective Organizing and culture-responsive organizations –Global and entrepreneurial organizing (3 Hrs.) Manager inventory chart-matching person with the job-system approach to selection (3 Hrs.) Job design-skills and personal characteristics needed in managers-selection process, techniques and instruments (3 Hrs.)	9	20%
VI	**Leading and Controlling:** Leading Vs Managing – Trait approach and Contingency approaches to leadership - Dimensions of Leadership (3 Hrs.) - Leadership Behavior and styles – Transactional and Transformational Leadership (3 Hrs.) Basic control process- control as a feedback system – Feed Forward Control – Requirements for effective control – control techniques – Overall controls and preventive controls – Global controlling (3 Hrs.)	9	20%
END SEMESTER EXAM			

Question Paper Pattern

Max. marks: 100, Time: 3 hours .
The question paper shall consist of three parts

Part A: 4 questions uniformly covering modules I and II. Each question carries 10 marks Students will have to answer any three questions out of 4 (3X10 marks =30 marks)
Part B : 4 questions uniformly covering modules III and IV. Each question carries 10 marks Students will have to answer any three questions out of 4 (3X10 marks =30 marks)
Part C: 6 questions uniformly covering modules V and VI. Each question carries 10 marks Students will have to answer any four questions out of 6 (4X10 marks =40 marks)

Note: In all parts, each question can have a maximum of four sub questions, if needed.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC305	Microprocessor & Microcontroller	3-0-0-3	2016

Prerequisite: EC207 Logic Circuit Design

Course objectives:

1. To understand fundamental operating concepts of microprocessors and microcontrollers.
2. To communicate with various devices using controller.
3. To design a microcontroller based system with the help of the interfacing devices.
4. To program the controller to make various peripherals work for specified application.

Syllabus:

Microprocessors: 8085 architecture and its operation, microprocessor initiated operations and bus organization, pin configuration and functions, generation of control signals for external operations- fetch, IO/M, read/write, machine cycles and bus timings. Addressing modes, instruction set, instruction classification. Overview/concept of peripheral IC interfacing with 8085 microprocessor (8251, 8253, 8255, 8279). Simple examples in assembly language programming for 8085 (only for internal examination). Introduction to development tools: IDE, cross assembler, builder, linker and debugger.(not required for exam). Introduction to 8086 and comparison between 8086, 80286, 80386, 80486 and Pentium.

Microcontrollers: 8051- features, architecture, memory organization, registers, I/O ports, pin configuration and functions. Addressing modes, instruction set, instruction classification. Assembly language programming. Interrupts in 8051. Timer/Counter programming: Operating modes, time delay generation, Waveform generation. Serial communication: RS 232 interface, registers in UART, modes of operation, programming examples for serial data transmission and reception. Interfacing of DIP switch, stepper motor, ADC, DAC, LEDs and seven segment displays, alphanumeric LCD module with 8051.

Expected outcome:

The students will be able to:

1. Distinguish various types of processor architectures.
2. Describe architectures, memory organization of 8085 microprocessor and 8051.
3. Develop programming skills in assembly for interfacing peripheral devices with 8051

Text Books:

1. Kenneth J. Ayala, The 8051 Microcontroller, Cengage learning, 3/e.
2. Lyla B.Das : Microprocessors and Microcontrollers, Pearson Education, India, 2011
3. Ramesh S. Goankar. 8085 Microprocessors Archiecture Application and Programming. Penram International, 5/e.

References:

1. Aditya P Mathur, Introduction to Microprocessor. Tata Mc Graw – Hill
2. Han Way Hung, “PIC Microcontroller, An introduction to software and hardware interfacing “, Cenage learning.
3. I.Scott Mackenzie, Raphel C.-W Phan, The 8051 microcontroller, 4th edition.
4. Muhammed Ali Mazidi, The 8051 Microcontroller and Embedded Systems, Pearson Education, 2nd edition
5. Nagoorkani, Microprocessors and Microcontrollers 2e, McGraw Hill Education India, 2012.
6. Soumitra Kumar Mandal. Microprocessors and Microcontrollers Architecture, Programming & Interfacing Using 8085, 8086 and 8051, McGraw Hill Education (2011).
7.

Course Plan			
Module	**Course content**	**Hours**	**End Sem. Exam Marks**
I	Microprocessors: Introduction, organization of a microprocessor based system, evolution of microprocessors, 8085 architecture and its operation, microprocessor initiated operations and bus organization, pin configuration and functions, generation of control signals for external operations-fetch, IO/M, read/write.	5	**15**
II	Machine cycles and bus timings, Addressing modes, instruction set instruction classification.	4	**15**
	Overview/concept of peripheral IC interfacing with 8085 microprocessor (8251, 8253, 8255, 8279).	3	
	Simple examples in assembly language programming for 8085 (only for internal examination)	2	**0**
	Introduction to development tools: IDE, cross assembler, builder, linker and debugger.(not required for exam)	3	
FIRST INTERNAL EXAM			
III	Introduction to 8086 and comparison between 8086,80286,80386,80486 and Pentium	2	**15**
	Microcontrollers: Introduction, comparison between microprocessors and microcontrollers, microcontroller families, 8051- features, architecture, memory organization, registers, I/O ports, pin configuration and functions.	6	
IV	Addressing modes, instruction set, instruction classification.	2	**15**
	Assembly language programming examples for 8051.	3	
SECOND INTERNAL EXAM			
V	Interrupts in 8051: Types, interrupt source, interrupt handling and programming	2	**20**
	Timer/Counter programming: Operating modes, time delay generation, Waveform generation.	2	
	Serial communication: RS 232 interface, registers in UART, modes of operation, programming examples for serial data transmission and reception	2	
VI	Interfacing: Interfacing (block schematic and assembly language programming) of DIP switch, stepper motor, ADC, DAC, LEDs and seven segment displays, alphanumeric LCD module with 8051.	6	**20**
END SEMESTER EXAM			

Question Paper Pattern (End semester exam)

Max. Marks: 100 **Time: 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 80 % for theory and 20% for logical/numerical problems and programming.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC307	Power Electronics & Instrumentation	3-0-0-3	2016

Prerequisite: EC205 Electronic Circuits

Course objectives:

1. To provide an insight on the concepts of Power Electronics and Electronic instruments.
2. To study the applications of Power electronics such as Switched mode regulators and inverters.
3. To develop understanding of the concept of Transducers and Digital instruments.

Syllabus:

Power semiconductor switches and its static and dynamic characteristics. Switched mode regulators, SMPS, Switched mode inverters, UPS.

Performance characteristics of instruments, Measurement of passive components, Different Transducers, Digital Instruments.

Expected outcome:

The students will be able:

1. To understand the concepts of Power Electronics and the various applications.
2. To get an insight on various electronic instruments, their configuration and measurements using them.
3. To understand the principle of operation of Transducers

Text Books:

1. Bell D. A., Electronic Instrumentation and Measurements, Oxford University Press, 2003.
2. Rashid M. H., "Power Electronics Circuits, Devices and Applications", Prentice Hall India, Third Edition, New Delhi.
3. Umanand L., Power Electronics Essentials and Applications, Wiley India, 2015.

References:

1. Daniel W. Hart, Power Electronics, McGraw Hill, 2011.
2. Doeblin E., Measurement Systems, 5/e, McGraw Hill, 2003.
3. Helfrick A. D. and W. D. Cooper: Modern Electronic Instrumentation and Measurement Techniques, 5/e, PHI, 2003.
4. Mandal, Power Electronics 1e, McGraw Hill Education India, 2014
5. Mohan N. and T. M. Undeland, Power Electronics: Converters, Applications and Design, John Wiley, 2007.
6. Nakra, Instrumentation, Measurement and Analysis,4e, Mc Graw –Hill Education New Delhi,2016
7. Patranabis D., Principles of Electronic Instrumentation, PHI, 2008.

Course Plan			
Module	**Course content**	**Hours**	**End Sem. Exam Marks**
I	Linear Electronics versus Power Electronics - Power semiconductor switches.	1	**15**
	Power diodes-structure, static and dynamic characteristics	2	
	Power transistors - Power BJT, Power MOSFET, GTO and IGBT	3	
	Steady state and switching characteristics of Power BJT, Power MOSFET and IGBT.	2	
II	Introduction to Switched mode regulators	1	**15**
	Buck, Boost and Buck-Boost DC-DC converters	2	
	Waveforms and expression of DC-DC converters for output voltage, voltage and current ripple under continuous conduction mode. (Derivation not required)	1	
	Isolated converters - Flyback, Forward, Push Pull, Half Bridge and Full Bridge Converters - waveforms and governing equations. (Derivation not required)	3	
FIRST INTERNAL EXAM			
III	Overview of SMPS, Switched mode inverters- Principles of PWM switching schemes.	2	**15**
	Single phase inverters - half bridge, full bridge and push pull.	2	
	UPS - on line and off line.	1	
	Three phase inverters - PWM and Space vector modulation in three phase inverters.	3	
IV	Generalized configurations of instruments - Functional elements. Classification of instruments	1	**15**
	Generalized performance characteristics of instruments - Static characteristics and Dynamic characteristics.	2	
	Measurement of: resistance using Wheastone's bridge, inductance using Maxwell-Wien bridge, and capacitance using Schering's bridge.	2	
SECOND INTERNAL EXAM			
V	Transducers - Classification, Selection of transducers.	1	**20**
	Resistance transducers - Principle of operation, strain gauge.	2	
	Inductive Transducers: LVDT.	2	
	Capacitive transducers - different types, capacitor microphone, Hall Effect transducer, proximity transducers.	2	
VI	Electronic Multimeter, Audio Power Meter, RF power meter	2	**20**
	Digital Instruments - Basics, digital measurement of time, phase, frequency and digital voltmeter.	2	
	Frequency synthesizer, Spectrum analyzers, Logic State analyzers (block diagram only).	1	

	Digital storage oscilloscope – Working Principle, controls and applications.	2	
END SEMESTER EXAM			

Question Paper Pattern (End Sem Exam)

Max. Marks: 100 **Time: 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 100 % for theory.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC333	Digital Signal Processing Lab	0-0-3-1	2016

Prerequisite:
EC 213 Electronics Design Automation Lab, EC 202 Signals & Systems

Course objectives:

- To enable the students to explore the concepts of design, simulation and implementation of various systems using MATLAB/SciLab/OCTAVE and DSP kit.

List of Experiments:

Part A: Experiments on Digital Signal Processor/ DSP kits: (All experiments are mandatory)

1. Generation of sine wave and standard test signals.
2. Convolution : Linear and Circular
3. Real Time FIR Filter implementation (Low-pass, High-pass and Band-pass) by inputting a signal from the signal generator
4. Real Time IIR Filter implementation (Low-pass, High-pass and Band-pass) by inputting a signal from the signal generator
5. Sampling of analog signal and study of aliasing.

Part B: Experiments based on MATLAB/SciLab/OCTAVE (7 experiments are mandatory)

1. Generation of Waveforms (Continuous and Discrete)
2. Verification of Sampling Theorem.
3. Time and Frequency Response of LTI systems (First and second order).
4. Linear Convolution, Circular Convolution and Linear Convolution using Circular Convolution.
5. To find the DFT and IDFT for the given input sequence.
6. Linear convolution using DFT (Overlap-add and Overlap-Save methods).
7. To find the DCT and IDCT for the given input sequence.
8. To find FFT and IFFT for the given input sequence.
9. FIR and IIR filter design using Filter Design Toolbox.
10. FIR Filter (Low-pass, High-pass and Band-pass)design (Window method).
11. IIR Filter (Low-pass, High-pass and Band-pass)design (Butterworth and Chebychev).
12. Generation of AM, FM & PWM waveforms and their spectrum.
13. Generation of DTMF signal.
14. Study of sampling rate conversion (Decimation, Interpolation, Rational factor).
15. Filtering of noisy signals
16. Implementation of simple algorithms in audio processing (delay, reverb, flange etc.).
17. Implementation of simple algorithms in image processing (detection, de-noising, filtering etc.)

Expected outcome:
The students will be able to:
Design, simulate and realize various systems related to DSP.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC335	Power Electronics & Instrumentation Lab	0-0-3-1	2016

Prerequisite: NIL

Course objectives:

- To design and implement basic power electronic circuits
- To study the working of transducers
- To train the usage of Digital Instruments

List of Experiments (8 experiments mandatory):

Cycle I (Four mandatory)

1. Design and Set up DC-DC converter
2. Design and Set up Push pull DC- DC Converter
3. Design and Set up Buck DC-DC Converters
4. Design and Set up Simple SMPS
5. Design and Set up Half bridge and full bridge converters
6. Design and Set up basic Inverter Circuits

Cycle II (Four mandatory)

7. Transducer measurements using diode thermometer
8. Transducer measurements using LVDT
9. Transducer measurements using Strain gauge.
10. Transducer measurements using Pressure transducer.
11. Transducer measurements using Thermocouple & RTDS
12. Transducer measurements using Photocells

Desired Experiment

13. Study of Digital LCR meter, Frequency synthesizer, Spectrum analyzer and Logic State analyzer application.

Expected outcome:

The students will be able to:

1. Design and demonstrate basic power electronic circuits.
2. Use transducers for application.
3. Function effectively as an individual and in a team to accomplish the given task.

Course code	Course Name	L-T-P - Credits	Year of Introduction
**341	DESIGN PROJECT	0-1-2-2	2016

Prerequisite : Nil

Course Objectives

- To understand the engineering aspects of design with reference to simple products
- To foster innovation in design of products, processes or systems
- To develop design that add value to products and solve technical problems

Course Plan

Study : Take minimum three simple products, processes or techniques in the area of specialisation, study, analyse and present them. The analysis shall be focused on functionality, strength, material, manufacture/construction, quality, reliability, aesthetics, ergonomics, safety, maintenance, handling, sustainability, cost etc. whichever are applicable. Each student in the group has to present individually; choosing different products, processes or techniques.

Design: The project team shall identify an innovative product, process or technology and proceed with detailed design. At the end, the team has to document it properly and present and defend it. The design is expected to concentrate on functionality, design for strength is not expected.

Note : The one hour/week allotted for tutorial shall be used for discussions and presentations. The project team (not exceeding four) can be students from different branches, if the design problem is multidisciplinary.

Expected outcome.

The students will be able to

i. Think innovatively on the development of components, products, processes or technologies in the engineering field
ii. Analyse the problem requirements and arrive workable design solutions

Reference:

Michael Luchs, Scott Swan, Abbie Griffin, 2015. Design Thinking. 405 pages, John Wiley & Sons, Inc

Evaluation

First evaluation (Immediately after first internal examination)	20 marks
Second evaluation (Immediately after second internal examination)	20 marks
Final evaluation (Last week of the semester)	60 marks

Note: All the three evaluations are mandatory for course completion and for awarding the final grade.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC360	Soft Computing	3-0-0 -3	2016

Prerequisite: NIL

Course objectives:

1. To familiarize various components of soft computing like fuzzy logic, neural networks and genetic algorithm.
2. To give an overview of fuzzy Logic and to understand the concepts and terminologies of fuzzy systems
3. To give a description on artificial neural networks with its advantages and application.
4. To study the fundamentals of Genetic Algorithm (GA).
5. To understand the concepts of hybrid systems.

Syllabus:

Fuzzy sets and systems. Neural Networks - Applications - typical architecture, pattern Classification and pattern Association. Fundamentals of Genetic Algorithm, AI search algorithm and hybrid structure.

Expected outcome:

The students will be able to:

1. Identify and describe soft computing techniques and their roles in building intelligent Machines.
2. Apply fuzzy logic and reasoning to handle uncertainty and solve engineering problems
3. Recognize the feasibility of applying a soft computing methodology for a particular Problem.
4. Apply neural networks to pattern classification and regression problems.
5. Apply genetic algorithms to combinatorial optimization problems

Text Books:

1. D.E. Goldberg, "Genetic Algorithms: Search, Optimization and Machine Learning", Addison Wesley,N.Y, 1989.
2. Laurene V. Fausett, (1993) “Fundamentals of Neural Networks: Architecture, Algorithms and Applications", Prentice Hall.
3. Timothy J. Ross, “Fuzzy Logic with Engineering Applications” Wiley India.

References:

1. Ibrahim A. M., Introduction to Applied Fuzzy Electronics, PHI, 2013.
2. J. Yen and R. Langari, Fuzzy Logic, Intelligence, Control and Information, Pearson Education.
3. K.H.Lee, First Course on Fuzzy Theory and Applications, Springer-Verlag.
4. Lin C. T. and C.S. G. Lee, Neural Fuzzy Systems, Prentice Hall, 1996.
5. S. Rajsekaran & G.A. Vijayalakshmi Pai, “Neural Networks, Fuzzy Logic and Genetic Algorithm: Synthesis and Applications” Prentice Hall of India.
6. S.N. Sivanandan and S.N. Deepa, Principles of Soft Computing, Wiley India, 2007. ISBN: 10: 81-265-1075-7.

Course Plan			
Module	**Course content**	**Hours**	**End Sem. Exam Marks**
I	Soft computing: Introduction, soft computing vs hard computing, Fuzzy Computing, Neural Computing, Genetic Algorithms. applications of soft computing	2	**15**
	Introduction to fuzzy sets and systems-crispness, vagueness, uncertainty and fuzziness. Basics of fuzzy sets, membership functions, support of a fuzzy set height, normalized fuzzy set, alpha cuts.	3	
II	Type- 2 fuzzy sets. Operation on fuzzy set-complement, intersection, union, Demorgan's Law Equality & subset hood.	4	**15**
	Extension Principle and its application, Fuzzy relation-operations, projection, max-min, min-max composition, cylindrical extension.	3	
FIRST INTERNAL EXAM			
III	Reflexivity, symmetry and transitivity of fuzzy relations. Fuzzy prepositions, fuzzy connectives, linguistic variables, hedges.	4	**15**
	Approximate reasoning or fuzzy inference, Fuzzy rule based system. Fuzzification and defuzzification using centroid, centre of sums.	4	
IV	Introduction to Neural Networks - Applications –Biological neuron- Typical architecture of Artificial Neural Networks - Common activation function.	4	**15**
	McCulloh Pitts Neuron – Architecture, logic implementatons. Supervised and Unsupervised learning	4	
SECOND INTERNAL EXAM			
V	Linear Separability, Pattern Classification: Perceptrons	2	**20**
	Back propagation network and its architecture, Back propagation learning, back propagation algorithm	4	
VI	Genetic Algorithm Basic concepts, Initialization and selection, Survival of the Fittest - Fitness Computations.	5	**20**
	Operators - Cross over, Mutation.	3	
END SEMESTER EXAM			

Question Paper (End semester exam)

Max. Marks: 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 50 % for theory, derivation, proof and 50% for logical/numerical problems.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC361	Digital System Design	3-0-0-3	2016

Prerequisite: EC207 Logic Circuit Design

Course objectives:

1. To study synthesis and design of CSSN
2. To study synthesis and design of ASC
3. To study hazards and design hazard free circuits
4. To study PLA folding
1. To study architecture of one CPLDs and FPGA family

Syllabus:

Clocked synchronous networks, asynchronous sequential circuits, Hazards, Faults, PLA, CPLDs and FPGA

Expected outcome:

The student will be able:

1. To analyze and design clocked synchronous sequential circuits
2. To analyze and design asynchronous sequential circuits
3. To apply their knowledge in diagnosing faults in digital circuits, PLA
4. To interpret architecture of CPLDs and FPGA

Text Books:

1. Donald G Givone, Digital Principles & Design, Tata McGraw Hill, 2003
2. John F Wakerly, Digital Design, Pearson Education, Delhi 2002
3. John M Yarbrough, Digital Logic Applications and Design, Thomson Learning

References:

1. Miron Abramovici, Melvin A. Breuer and Arthur D. Friedman, Digital Systems Testing and Testable Design, John Wiley & Sons Inc.
2. Morris Mano, M.D.Ciletti, Digital Design, 5th Edition, PHI.
3. N. N. Biswas, Logic Design Theory, PHI
4. Richard E. Haskell, Darrin M. Hanna , Introduction to Digital Design Using Digilent FPGA Boards, LBE Books- LLC
5. Samuel C. Lee, Digital Circuits and Logic Design, PHI
6. Z. Kohavi, Switching and Finite Automata Theory, 2nd ed., 2001, TMH

Course Plan

Module	Course content	Hours	End Sem. Exam Marks
I	Analysis of clocked Synchronous Sequential Networks(CSSN)	2	15
	Modelling of CSSN – State assignment and reduction	1	
	Design of CSSN	2	
	Iterative circuits	1	
	ASM Chart and its realization	2	
II	Analysis of Asynchronous Sequential Circuits (ASC)	2	15
	Flow table reduction- Races in ASC	1	
	State assignment problem and the transition table- Design of AS	2	
	Design of Vending Machine controller.	2	

FIRST INTERNAL EXAM			
III	Hazards – static and dynamic hazards – essential	1	**15**
	Design of Hazard free circuits – Data synchronizers	1	
	Mixed operating mode asynchronous circuits	1	
	Practical issues- clock skew and jitter	1	
	Synchronous and asynchronous inputs – switch bouncing	2	
IV	Fault table method – path sensitization method – Boolean difference method	2	**15**
	Kohavi algorithm	2	
	Automatic test pattern generation – Built in Self Test(BIST)	3	
SECOND INTERNAL EXAM			
V	PLA Minimization - PLA folding	2	**20**
	Foldable compatibility Matrix- Practical PLA	2	
	Fault model in PLA	1	
	Test generation and Testable PLA Design.	3	
VI	CPLDs and FPGAs - Xilinx XC 9500 CPLD family, functional block diagram– input output block architecture - switch matrix	3	**20**
	FPGAs – Xilinx XC 4000 FPGA family – configurable logic block - input output block, Programmable interconnect	3	
END SEMESTER EXAM			

Question Paper Pattern (End semester exam)

Max. Marks: 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 50 % for theory, derivation, proof and 50% for logical/numerical problems.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC363	Optimization Techniques	3-0-0-3	2016

Prerequisite: NIL

Course objectives:

1. To understand the need and origin of the optimization methods.
2. To get a broad picture of the various applications of optimization methods used in engineering.
3. To define optimization problem and its various components

Syllabus: Engineering applications of optimization, Formulation of design problems as mathematical programming problems, objective function, constraints, classification of optimization problems/techniques, necessary and sufficient conditions for optimality, uni-modality, convexity, Mathematical formulation of LP Problems, Reduction of a LPP to the standard form. Feasible solutions, Graphical solution methods, optimality conditions, degeneracy, Simplex algorithm, Duality in linear programming, Transportation Problem, Game theory, Network path models, Nonlinear unconstrained optimization, Modern methods of optimization, Genetic algorithm. Introduction to optimization tools and software.

Expected outcome:

The students will (i) have a thorough understanding of optimization techniques
(ii)l be able to formulate and solving the engineering optimization problems

Text Books:

1. H.A. Taha, " Operations Research", 5/e, Macmillan Publishing Company, 1992.
2. Kalynamoy Deb. "Optimization for Engineering Design- Algorithms and Examples", Prentice-Hall of India Pvt. Ltd., New Delhi
3. Singiresu S Rao, "Engineering optimization Theory and Practice", New Age International, 2009

References:

1. A. Ravindran, D. T. Phillips, J. J. Solberg, Operations Research – Principles and Practice, John Wiley and Sons.
2. Ashok D Belegundu, Tirupathi R Chandrupatla, "Optimization concepts and Application in Engineering", Pearson Education.
3. Hadley, G. "Linear programming", Narosa Publishing House, New Delhi
4. J. S. Arora, Introduction to Optimum Design, McGraw-Hill Book Company.
5. Kanti Swarup, P.K.Gupta and Man Mohan, Operations Research, Sultan Chand and Sons
6. Papalambros & Wilde, Principles of Optimal Design, Cambridge University Press, 2008

Course Plan			
Module	**Course content**	**Hours**	**End Sem. Exam Marks**
I	Introduction: Engineering applications of optimization, Formulation of design problems as mathematical programming problems, objective function, constraints, classification of optimization problems/techniques.	2	**15**
	Optimization techniques: Classical optimization, unconstrained single and multivariable minimization- necessary and sufficient conditions for optimality, uni-modality, convexity.	5	
II	Linear programming problems-I: Mathematical formulation of LP Problems, slack, surplus and artificial variables. Reduction of a LPP to the standard form, feasible solutions. Graphical solution method, simplex algorithm and solution using tabular method, optimality conditions and degeneracy. Duality in linear programming	7	**15**
FIRST INTERNAL EXAM			
III	Transportation Problem: Formulation of transportation problem, Basic feasible solution using different methods- East West corner method, Vogel approximation method, Optimality methods, MODI method, Unbalanced transportation problem	7	**15**
IV	Game Theory: Introduction, 2- person zero – sum game; Saddle point; Mini-Max and Maxi-Min Theorems (statement only); Graphical solution (2x n, m x 2 game), dominance property. Network path Models: Tree Networks – Minimal Spanning Tree - Prim's Algorithm. Shortest path problems- solution methods – Dijkstra's Method.	7	**15**
SECOND INTERNAL EXAM			
V	Nonlinear unconstrained optimization: Single variable optimization methods- Fibonacci search method, Newton-Raphson method. Multi-variable methods- Hook-Jeeves pattern search method, Cauchy's (steepest descent) method.	7	**20**
VI	Modern methods of optimization: Introduction to Genetic algorithm, Cross over, Mutation, Reproduction, Simple examples of applications in electronics engineering	5	**20**
	Introduction to optimization tools and softwares. Solution of optimization Problems using MATLAB.	2	**0**
END SEMESTER EXAM			

Question Paper Pattern (End sem. Exam.)

Max. Marks: 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 30 % for theory and 70% for logical/numerical problems, derivation and proof.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC365	Biomedical Engineering	3-0-0-3	2016

Prerequisite: Nil

Course objectives:

1. To introduce student to basic biomedical engineering technology
2. To understand the anatomy & physiology of major systems of the body in designing equipment for medical treatments.
3. To impart knowledge about the principle and working of different types of bio-medical electronic equipment/devices.

Syllabus:

Human body-overview, Physiological systems of body, Measurement of physiological parameters, Assisting and therapeutic devices, Medical laboratory equipments, Telemetry in patient care, Patient safety, Medical imaging system

Expected outcome:

The students will be able:

1. To understand diagnosis and therapy related equipments.
2. To understand the problem and identify the necessity of equipment for diagnosis and therapy.
3. To understand the importance of electronics engineering in medical field.
4. To understand the importance of telemetry in patient care

Text Books:

1. K S Kandpur, "Hand book of Biomedical instrumentation", Tata McGraw Hill 2nd e/d.
2. Leslie Cromwell, Fred J. Weibell, Erich A. Pfeiffer, Biomedical Instrumentation and Measurements, PHI, 2nd Edition, 2004

References:

1. Barbara Christe, Introduction to Biomedical Instrumentation, Cambridge University Press, 2008.
2. J. J. Carr, "Introduction to Biomedical Equipment Technology", Pearson Education 4th e/d.
3. John G Webster, "Medical Instrumentation application and design", John Wiley 3rd e/d.
4. Richard Aston, "Principle of Biomedical Instrumentation and Measurement". Merrill Education/Prentice Hall.

Course Plan

Module	Course content	Hours	End Sem. Exam Marks
I	Introduction to bio-medical instrumentation system, overview of anatomy and physiological systems of the body.	1	15
	Sources of bio-electric potential: Resting and action potential, propagation of action potentials. Bioelectric potentials examples (ECG, EEG, EMG, ERG, EOG, EGG, etc introduction only.)	2	
	Electrode theory: Nernst relation Bio potential electrodes: Microelectrodes, skin surface electrodes, needle electrodes.	1	

	Instrumentation for clinical laboratory: Bio potential amplifiers- instrumentation amplifiers, carrier amplifiers, isolation amplifiers, chopper amplifiers	2	
II	Heart and cardiovascular system (brief discussion), electro conduction system of the heart. Electrocardiography, ECG machine block diagram, ECG lead configurations, ECG recording system, Einthoven triangle, analysis of ECG signals.	3	**15**
	Measurement of blood pressure: Direct, indirect and relative methods of blood pressure measurement, auscultatory method, oscillometric and ultrasonic non-invasive pressure measurements.	2	
	Measurement of blood flow: Electromagnetic blood flow meters and ultrasonic blood flow meters.	2	
FIRST INTERNAL EXAM			
III	The human nervous system. Neuron, action potential of brain, brain waves, types of electrodes, placement of electrodes, evoked potential, EEG recording, analysis of EEG.	2	**15**
	Electromyography: Nerve conduction velocity, instrumentation system for EMG.	1	
	Physiology of respiratory system (brief discussion), Respiratory parameters, spirometer, body plethysmographs, gas exchange and distribution.	2	
	Instruments for clinical laboratory: Oxymeters, pH meter, blood cell counter, flame photometer, spectrophotometer	3	
IV	Therapeutic Equipments: Principle, block schematic diagram, working and applications of : pacemakers, cardiac defibrillators, heart–lung machine, dialyzers, surgical diathermy equipment, ventilators	6	**15**
SECOND INTERNAL EXAM			
V	Medical Imaging systems (Basic Principle only): X-ray imaging - Properties and production of X-rays, X-ray machine, applications of X-rays in medicine.	2	**20**
	Computed Tomograpy: Principle, image reconstruction, scanning system and applications.	2	
	Ultrasonic imaging systems: Basic pulse echo system, propagation of ultrasonic through tissues and reflections, display types, A-Scan, B-Scan, M-Scan, applications, real-time ultrasonic imaging systems and probes.	3	
VI	Magnetic Resonance Imaging – Basic NMR components, Biological effects and advantages of NMR imaging	3	**20**
	Biomedical Telemetry system: Components of biotelemetry system, application of telemetry in medicine, single channel telemetry system for ECG and temperature	2	
	Patient Safety: Electric shock hazards, leakage current, safety codes for electro medical equipments	1	
END SEMESTER EXAM			

Question Paper Pattern (End Sem. Exam)

Maximum Marks: 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 100 % for theory.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC302	Digital Communication	4-0-0-4	2016

Prerequisite: EC204 Signals and Systems, EC208 Analog Communication

Course Objectives:

- To understand the concept of Digital representation of analog source
- To understand the Performance comparison various pulse modulation schemes
- To discuss Inter Symbol Interference (ISI) problem in digital communication and to derive the Nyquist Criteria for zero ISI in data Transmission
- To analyse the need for introducing ISI in controlled manner
- To understand signal space representation of signal using Gram Schmidt orthonormalisation procedure
- To analyse the error probability for different modulation schemes like BPSK, BFSK, QPSK etc.
- To understand the principle of spread spectrum communication and to illustrate the concept of FHSS and DSSS
- To understand various Multiple Access Techniques

Syllabus: Overview of Random variables and Random process, Overall picture and relevance of digital communication, Digital Pulse modulation, Signal space concepts, Matched filter receiver, Review of Gaussian random process, Digital band pass modulation schemes, Detection of signals in Gaussian noise, Pseudo–noise sequences, Importance of synchronization, Spread spectrum communication, Diversity techniques, Multiple Access Techniques.

Expected Outcome

The students will be able to

i. Illustrate the Digital representation of analog source
ii. Compare the performance of various Digital Pulse Modulation Schemes
iii. Apply the knowledge of ISI problems in Digital communication to derive Nyquist criteria for zero ISI
iv. Analyse the need for introducing ISI in Digital Communication in a controlled manner
v. Construct signal space representation of signal using Gram Schmidt orthonormalisation procedure
vi. Compare the error probability for different digital modulation schemes like BPSK, BFSK, QPSK etc.
vii. Describe the principle of spread spectrum communication and to illustrate the concept of FHSS and DSSS
viii. Understand various Diversity Techniques

Text Books:

1. John G. Proakis, Masoud Salehi, Digital Communication, McGraw Hill Education Edition, 2014
2. Nishanth N, Digital Communication, Cengage Learning India , 2017
3. Ramakrishna Rao, Digital communication, Tata McGraw Hill Education Pvt. Limited.
4. Simon Haykin, Communication Systems, 4/e Wiley India, 2012.

References:
1. Couch: Analog and Digital Communication. 8e, Pearson Education India, 2013.
2. H.Taub and Schilling Principles of Communication Systems, , TMH, 2007
3. K.Sam Shanmugham, Digital and Analog Communication Systems, John Wiley & Sons
4. Pierre Lafrance ,Fundamental Concepts in Communication, Prentice Hall India.
5. Sheldon.M.Ross, "Introduction to Probability Models", Academic Press, 7th edition.
6. Sklar: Digital Communication, 2E, Pearson Education.
7. T L Singal, Digital Communication, McGraw Hill Education (India) Pvt Ltd, 2015

Course Plan

Module	Course content	Hours	End Sem. Exam Marks
I	**Overview of Random variables and Random process:** Random variables–continuous and Discrete, random process-Stationarity, Autocorrelation and power spectral density, Transmission of Random Process through LTI systems, PSD, AWGN	3	15
	Pulse Code Modulation (PCM): Pulse Modulation, Sampling process, Performance comparison of various sampling techniques Aliasing, Reconstruction, PAM, Quantization, Noise in PCM system	3	
	Modifications of PCM: Delta modulation, DPCM, ADPCM, ADM, Performance comparison of various pulse modulation schemes, Line codes, PSD of various Line codes	4	
II	**Transmission over baseband channel:** Matched filter, Inter Symbol Interference (ISI), Nyquist Criteria for zero ISI, Ideal solution, Raised cosine spectrum, Eye Pattern	4	15
	Correlative Level Coding - Duobinary coding, precoding, Modified duobinary coding, Generalized Partial response signalling.	3	
FIRST INTERNAL EXAM			
III	**Signal Space Analysis:** Geometric representation of signals, Gram Schmidt orthogonization procedure.	3	15
	Transmission Over AWGN Channel: Conversion of the continuous AWGN channel into a vector channel, Likelihood function, Maximum Likelihood Decoding, Correlation Receiver	4	
IV	**Digital Modulation Schemes:** Pass band transmission model**,** Coherent Modulation Schemes- BPSK, QPSK, BFSK**.** Non-Coherent orthogonal modulation schemes**,** Differential Phase Shift Keying (DPSK)	4	15
	Detection of Binary modulation schemes in the presence of noise**,** BER for BPSK, QPSK, BFSK	5	
SECOND INTERNAL EXAM			
V	**Pseudo–noise sequences**: Properties of PN sequences. Generation of PN Sequences, generator polynomials, Maximal length codes and Gold Codes.	3	20

	Importance of synchronization: Carrier, frame and symbol/chip synchronization techniques.	2	
	Spread spectrum communication: Direct sequence spread spectrum with coherent binary phase shift keying, Processing gain, Probability of error, Anti-jam Characteristics, Frequency Hop spread spectrum with MFSK, Slow and Fast frequency hopping.	4	
VI	**Multipath channels:** classification, Coherence time, Coherence bandwidth, Statistical characterization of multi path channels, Binary signalling over a Rayleigh fading channel.	3	**20**
	Diversity techniques: Diversity in time, frequency and space.	2	
	Multiple Access Techniques: TDMA, FDMA, CDMA and SDMA – RAKE receiver, Introduction to Multicarrier communication- OFDM	5	
END SEMESTER EXAM			

Question Paper Pattern (End Semester Exam)

Maximum Marks : 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 30% for theory and 70% for logical/numerical problems, derivation and proof.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC304	VLSI	3-0-0-3	2016

Prerequisite: EC203 Solid State Devices, EC204 Analog Integrated Circuit.

Course objectives:

- To give the knowledge about IC Fabrication Techniques
- To impart the skill of analysis and design of MOSFET and CMOS logic circuits.

Syllabus:

IC Fabrication Technology**,** CMOS IC Fabrication Sequence, CMOS inverters, Design rules, Static CMOS Design, Dynamic CMOS circuits, Pass transistor, Read Only Memory, Random Access Memory, Sense amplifiers, Adders, multipliers, Testing of VLSI circuits.

Expected outcome:

The students will be able to design and analyse various MOSFET and CMOS logic circuits.

Text Books:

1. John P Uyemura, Introduction to VLSI Circuits and Systems, Wiley India, 2006
2. S.M. SZE, VLSI Technology, 2/e, Indian Edition, McGraw-Hill,2003

References:

1. Jan M.Rabaey, Digital Integrated Circuits- A Design Perspective, Prentice Hall, Second Edition, 2005.
2. Neil H.E. Weste, Kamran Eshraghian, Principles of CMOS VLSI Design- A Systems Perspective, Second Edition. Pearson Publication, 2005
3. Razavi - Design of Analog CMOS Integrated Circuits,1e, McGraw Hill Education India Education, New Delhi, 2003.
4. Sung –Mo Kang & Yusuf Leblebici, CMOS Digital Integrated Circuits- Analysis & Design, McGraw-Hill, Third Ed., 2003.
5. Yuan Taur & Ning, Fundamentals of Modern VLSI Devices, Cambridge University Press, 2008

Course Plan

Module	Course content	Hours	End Sem. Exam Marks
I	**Material Preparation**- Purification, Crystal growth (CZ and FZ process), wafer preparation **Thermal Oxidation-** Growth mechanisms, Dry and Wet oxidation, Deal Grove model.	4	15
	Diffusion- Fick's Laws, Diffusion with constant surface concentration and from a constant source, diffusion techniques. **Ion implantation**-Technique, Range Theory, annealing.	3	
II	**Epitaxy :** Vapour phase epitaxy and molecular beam epitaxy **Lithography**- Photo lithographic sequence, Electron Beam Lithography, Etching and metal deposition	4	15
	Methods of isolation Circuit component fabrication: transistor, diodes, resistors, capacitors, N-well CMOS IC Fabrication Sequence	3	
	FIRST INTERNAL EXAM		
III	**CMOS inverters**- DC characteristics, switching characteristics, power dissipation	4	15

	Layout Design rules, Stick Diagram and layout of CMOS Inverter, two input NAND and NOR gates	4	
IV	**MOSFET Logic Design** -Pass transistor logic, Complementary pass transistor logic and transmission gate logic , realization of functions	6	**15**
SECOND INTERNAL EXAM			
V	**Read Only Memory**-4x4 MOS ROM Cell Arrays(OR,NOR,NAND) **Random Access Memory** –SRAM-Six transistor CMOS SRAM cell, DRAM –Three transistor and One transistor Dynamic Memory Cell	4	**20**
	Sense amplifiers –Differential Voltage Sensing Amplifiers Introduction to PLDs and FPGAs, Design of PLAs.	3	
VI	**Adders**- Static adder, Carry-By pass adder, Linear Carry-Select adder, Square- root carry- select adder **Multipliers**-Array multiplier	4	**20**
END SEMESTER EXAM			

Question Paper Pattern (End Semester Exam)

Maximum Marks : 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 70% for theory and 30% for logical/numerical problems, derivation and proof.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC306	Antenna & Wave Propagation	3-0-0-3	2016

Prerequisite: EC303 Applied Electromagnetic Theory

Course objectives:

- To learn the basic working of antennas.
- To study various antennas, arrays and radiation patterns of antennas.
- To understand various techniques involved in various antenna parameter measurements.
- To understand the propagation of radio waves in the atmosphere.

Syllabus:

Antenna and antenna parameters, Duality of antennas, Derivation of electromagnetic fields and directivity of short dipole and half wave dipole, Measurement of antenna parameters. Antenna arrays and design of Endfire, broadside, binomial and Dolphchebyshev arrays, Principles of practical antennas. Traveling wave antennas, principle and applications of V and rhombic antennas Principles of Horn, Parabolic dish antenna and Cassegrain antenna, Log periodic antenna array and Helical antenna. Design of rectangular Patch antennas. Principle of smart antenna, Radio wave propagation, Different modes, effect of earth's magnetic field. Fading and diversity techniques.

Expected outcome:

The student will be able to know:

i. The basic working of antennas.
ii. Various antennas, arrays and radiation patterns of antennas
iii. Various techniques involved in various antenna parameter measurements.
iv. The propagation of radio waves in the atmosphere.

Text Books:

1. Balanis, Antenna Theory and Design, 3/e, Wiley Publications.
2. John D. Krauss, Antennas for all Applications, 3/e, TMH.

References:

1. Collin R.E, Antennas & Radio Wave Propagation, McGraw Hill. 1985.
2. Jordan E.C. & K. G. Balmain, Electromagnetic Waves & Radiating Systems, 2/e, PHI.
3. Raju G.S.N., Antenna and Wave Propagation, Pearson, 2013.
4. Sisir K.Das & Annapurna Das, Antenna and Wave Propagation, McGraw Hill,2012
5. Terman, Electronics & Radio Engineering, 4/e, McGraw Hill.
6. Thomas A. Milligan, Modern Antenna Design, IEEE PRESS, 2/e, Wiley Inter science.

Course Plan			
Module	**Course content**	**Hours**	**End Sem. Exam Marks**
I	Basic antenna parameters - gain, directivity, beam solid angle, beam width and effective aperture calculations. Effective height - wave polarization - antenna temperature - radiation resistance - radiation efficiency - antenna field zones - principles of reciprocity. Duality of antennas.	7	**15**
II	Concept of retarded potential. Field, directivity and radiation resistance of a short dipole and half wave dipole. Measurement of radiation pattern, gain, directivity and impedance of antenna	7	**15**
FIRST INTERNAL EXAM			
III	Arrays of point sources - field of two isotropic point sources - principle of pattern multiplication - linear arrays of 'n' isotropic point sources. Grating lobes.	4	**15**
	Design of Broadside, Endfire & Binomial arrays. Design of DolphChebyshev arrays.	4	
IV	Basic principle of beam steering. Travelling wave antennas. Principle and applications of V and rhombic antennas. Principles of Horn, Parabolic dish antenna, Cassegrain antenna (expression for E, H andGain without derivation).	6	**15**
SECOND INTERNAL EXAM			
V	Principle of Log periodic antenna array and Helical antenna. Antennas for mobile base station and handsets.	3	**20**
	Design of rectangular Patch antennas. Principle of smart antenna.	3	
VI	Radio wave propagation , Modes , structure of atmosphere, sky wave propagation , effect of earth's magnetic field, Ionospheric abnormalities and absorption, space wave propagation, LOS distance	4	**20**
	Field strength of space wave, duct propagation, VHF and UHF Mobile radio propagation, tropospheric scatter propagation, fading and diversity techniques.	4	
END SEMESTER EXAM			

Question Paper Pattern (End semester exam)

Max. Marks : 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 50% for theory and 50% for logical/numerical problems, derivation and proof.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC308	Embedded Systems	3-0-0 -3	2016

Prerequisite: EC206 Computer Organization, EC305 Microprocessors & Microcontrollers

Course objectives:

- To have a thorough understanding of the basic structure and design of an Embedded System
- To study the different ways of communicating with I/O devices and standard I/O interfaces.
- To study the basics of RTOS for Embedded systems.
- To study the programming concepts of Embedded Systems
- To study the architecture of System-on-Chip and some design examples.

Syllabus: Introduction to Embedded Systems, Embedded system design process, Serial and parallel communication standards and devices, Memory devices and device drivers, Programming concepts of embedded programming - Embedded C++ and embedded java, Real Time Operating Systems Micro C/OS-II.

Expected outcome:

The students will be able to:

i. Understand the basics of an embedded system
ii. Develop program for an embedded system.
iii. Design, implement and test an embedded system.

Text Books:

1. David E. Simon, An Embedded Software Primer, Pearson Education Asia, First Indian Reprint 2000.
2. Wayne Wolf, Computers as Components: Principles of Embedded Computing System Design, Morgan Kaufman Publishers - Elsevier 3ed, 2008

References:

1. Frank Vahid and Tony Givargis, Embedded Systems Design – A Unified Hardware / Software Introduction, John Wiley, 2002
2. Iyer - Embedded Real time Systems, 1e, McGraw Hill Education New Delhi, 2003
3. K.V. Shibu, Introduction to Embedded Systems, 2e, McGraw Hill Education India, 2016.
3. Lyla B. Das, Embedded Systems: An Integrated Approach, 1/e , Lyla B. Das, Embedded Systems, 2012
4. Rajkamal, Embedded Systems Architecture, Programming and Design, TMH, 2003
5. Steve Heath, Embedded Systems Design, Newnes – Elsevier 2ed, 2002
6. Tammy Noergaard, Embedded Systems Architecture, A Comprehensive Guide for Engineers and Programmers, Newnes – Elsevier 2ed, 2012

Course Plan			
Module	**Course content**	**Hours**	**End Sem. Exam Marks**
I	Introduction to Embedded Systems– Components of embedded system hardware–Software embedded into the system – Embedded Processors - CPU architecture of ARM processor (ARM9) – CPU Bus Organization and Protocol.	4	**15**
	Design and Development life cycle model - Embedded system design process – Challenges in Embedded system design	3	
II	Serial Communication Standards and Devices - UART, HDLC, SCI and SPI.	3	**15**
	Serial Bus Protocols - I2C Bus, CAN Bus and USB Bus. Parallel communication standards ISA, PCI and PCI-X Bus.	3	
FIRST INTERNAL EXAM			
III	Memory devices and systems - memory map – DMA - I/O Devices – Interrupts - ISR – Device drivers for handling ISR – Memory Device Drivers – Device Drivers for on-board bus.	6	**15**
IV	Programming concepts of Embedded programming – Features of Embedded C++ and Embedded Java (basics only). Software Implementation, Testing, Validation and debugging, system-on-chip.	6	**15**
	Design Examples: Mobile phones, ATM machine, Set top box	1	**0**
SECOND INTERNAL EXAM			
V	Inter Process Communication and Synchronization -Process, tasks and threads –Shared data– Inter process communication - Signals – Semaphore – Message Queues – Mailboxes – Pipes – Sockets – Remote Procedure Calls (RPCs).	8	**20**
VI	Real time operating systems - Services- Goals – Structures - Kernel - Process Management – Memory Management – Device Management – File System Organization. Micro C/OS-II RTOS - System Level Functions – Task Service Functions – Memory Allocation Related Functions – Semaphore Related Functions. Study of other popular Real Time Operating Systems.	8	**20**
END SEMESTER EXAM			

Question Paper Pattern (End semester exam)

Maximum Marks : 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 100 % for theory.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC312	Object Oriented Programming	3-0-0-3	2016

Prerequisite: NIL

Course objectives:

- To introduce the Object Oriented Programming paradigm using C++ and Java as the languages.
- To learn simple Android application development from the fundamentals.

Syllabus:

Object Oriented Programming and basics of C++, Advanced features of C++ programming such as exception handling and templates. Object oriented features of Java and their implementation. Advanced features of Java including packages, multithreading and error management. Introduction to Android application development with a case study.

Expected outcome:

Tthe students will have:

i. A thorough understanding of the features of OOP like class construction, polymorphism and inheritance of C++ and Java.
ii. An understanding of advanced features of C++ such as templates, abstract classes and virtual functions.
iii. Knowledge of advanced features of Java such as multithreading, packages and error management.
iv. Skills in designing android application development.
v. Skills in debugging, deploying and testing mobile applications.

Text Books:

1. E. Balagurusamy, Object Oriented Programming with C++ and JAVA, McGrawHill, 2015
2. Hardy, Brian, and Bill Phillips, Android Programming: The Big Nerd Ranch Guide. Addison-Wesley Professional, 2013.
3. Yashwant P. Kanetkar, Let us C++, 2/e, BPB Publications, 2003

References:

1. Deitel, Harvey M., and Paul J. Deitel., Java how to program.,7th International edition." (2007): 390-420.
2. G. Booch, R. A. Maksimchuk, M. W. Engel, and B J. Young, Object-oriented Analysis and Design with Applications, Addison-Wesley, 3rd Edition, 2007.
3. Horstmann, Cay S., and Gary Cornell., Core Java 2: Volume I, Fundamentals, Pearson Education, 2002.
4. Samanta, Debasis, Object-Oriented programming with C++ and Java, PHI Learning Pvt. Ltd., 2006.
5. Stroustrup, Bjarne. The C++ programming language, Pearson Education India, 1986.
6. www.tutorialspoint.com/android/android_tutorial.pdf

Course Plan			
Module	**Course content**	**Hours**	**End Sem. Exam Marks**
I	Concepts of OOP – Introduction to OOP, Procedural Vs. Object Oriented Programming, Principles of OOP, Benefits and applications of OOP.	2	**15**
	Beginning with C++: Overview and Structure of C++ Program, Classes and Objects, Constructors and Destructors.	4	
II	Operator Overloading and Inheritance – Overloading Unary Operators, Overloading Binary Operators, Overloading Binary Operators using Friends, Manipulation of Strings Using Operators.	4	**15**
	Inheritance – Multilevel Inheritance, Multiple Inheritance, Hierarchical Inheritance, Hybrid Inheritance. Virtual Base Classes, Abstract Classes, Constructors in Derived Classes, Member Classes: Nesting of Classes	5	
FIRST INTERNAL EXAM			
III	Virtual Functions and Polymorphism – Pointers to objects, this pointer, Pointers to derived classes, Virtual functions, Virtual Constructors and Destructors.	6	**15**
IV	Programming with JAVA – Overview of Java Language, Classes Objects and Methods, Method Overloading and Inheritance, Overriding Methods, Final Variables and Methods. Interfaces, Packages, Multithreaded programming, Managing Errors and Exceptions.	8	**15**
SECOND INTERNAL EXAM			
V	Introduction to Android : Setting up Development Environment, Basic Building blocks – Activities, Services, Broadcast Receivers & Content providers, UI Components – Views & notifications, Components for communication – Intents & Intent Filters,	6	**20**
VI	Application Structure-Android Manifest.xml, uses-permission & uses-sdk, Layouts & Drawable Resources, First sample Application, Emulator-Android Virtual Device, Basic UI design, Styles & Themes, Content Providers-SQLite Programming, Case study –Develop an App to demonstrate database usage.	7	**20**
END SEMESTER EXAM			

Assignment:

1. Assignment for implementing virtual base class in C++ related to some application.
2. Assignment for implementing a simple interactive applet in Java (eg: calculator)
3. A group assignment on simple android mobile app (eg: managing students' details and rank calculation of a class).

Question Paper Pattern (End semester exam)

Maximum marks : 100 **Time : 3 hours**

The question paper shall consist of three parts. Part A covers modules I and II, Part B covers modules III and IV, and Part C covers modules V and VI. Each part has three questions uniformly covering the two modules and each question can have maximum four subdivisions. In each part, any two questions are to be answered. Mark patterns are as per the syllabus with 60 % for theory and 40% for logical/numerical problems, derivation and proof.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC332	Communication Engineering Lab (Analog & Digital)	0-0-3-1	2016

Prerequisite: EC204 Analog Integrated Circuit, EC208 Analog Communication Engineering.

Course objectives:

- To provide experience on design, testing and analysis of few electronic circuits used in communication engineering.

List of Experiments:

Cycle I (Six experiments are mandatory)

1. AM generation using discrete components.
2. AM using multiplier IC AD534 or AD633.
3. AM detection using envelope detector.
4. IF tuned amplifier.
5. FM using 555 IC.
6. FM generation and demodulation using PLL.
7. Frequency multiplier using PLL
8. Pre-emphasis and de-emphasis circuits
9. Analog signal sampling & Reconstruction

Cycle II (Six mandatory)

10. Generation of Pseudo Noise Binary sequence using Shift registers
11. Time Division Multiplexing and Demultiplexing
12. Generation & Detection of DM/SIGMA DELTA/ ADM
13. Generation & Detection of PAM/PWM/PPM
14. Generation & Detection of BPSK/DPSK/DEPSK
15. Generation & Detection of PCM
16. 16 QPSK Modulation and Demodulation

Expected outcome:

The students will be able to understand the basic concepts of circuits used in communication systems.

COURSE CODE	COURSE NAME	L-T-P-C	YEAR OF INTRODUCTION
EC334	Microcontroller Lab	0-0-3-1	2016

Prerequisite: EC305 Microprocessors & Microcontrollers

Course objectives:

1. To understand Assembly Language/embedded C programming of Microcontroller.
2. To interface simple peripheral devices to a Microcontroller.
3. To equip student groups to design and implement simple embedded systems.

List of Experiments:

PART –A (At least 6 experiments are mandatory)

Assembly Language Programming experiments using 8051 Trainer kit.

1. Data transfer/exchange between specified memory locations.
2. Largest/smallest from a series.
3. Sorting (Ascending/Descending) of data.
4. Addition / subtraction / multiplication / division of 8/16 bit data.
5. Sum of a series of 8 bit data.
6. Multiplication by shift and add method.
7. Square / cube / square root of 8 bit data.
8. Matrix addition.
9. LCM and HCF of two 8 bit numbers.
10. Code conversion – Hex to Decimal/ASCII to Decimal and vice versa.

PART –B (At least 4 experiments are mandatory)

Interfacing experiments using 8051 Trainer kit and interfacing modules.

1. Time delay generation and relay interface.
2. Display (LED/Seven segments/LCD) and keyboard interface.
3. ADC interface.
4. DAC interface with wave form generation.
5. Stepper motor and DC motor interface.
6. Realization of Boolean expression through port.
7. Elevator interfacing.

PART -C(At least 2 experiments are mandatory)

Programming / interfacing experiments with IDE for 8051/PIC/MSP/Arduino/Raspberry Pi based interfacing boards/sensor modules (Direct downloading of the pre-written ALP/'C'/Python programs can be used).

1. Relay control
2. Distance measurement.
3. Temperature measurement / Digital Thermometer
4. Txr-Rxr interface.
5. Alphanumeric LCD display interface.
6. Simple project work including multiple interfaces.

Expected outcome:

The students will be able to:

1. Program Micro controllers.
2. Interface various peripheral devices to Micro controller.
3. Function effectively as an individual and in a team to accomplish the given task.

Course code	Course Name	L-T-P - Credits	Year of Introduction
**352	Comprehensive Examination	0-1-1-2	2016
Prerequisite : Nil			

Course Objectives

- To assess the comprehensive knowledge gained in basic courses relevant to the branch of study
- To comprehend the questions asked and answer them with confidence.

Assessment

Oral examination – To be conducted by the college (@ three students/hour) covering all the courses up to and including V semester– 50 marks

Written examination - To be conducted by the Dept. on the date announced by the University– common to all students of the same branch – objective type (1 hour duration)– 50 multiple choice questions (4 choices) of 1 mark each covering the six common courses of S1&S2 and six branch specific courses listed – questions are set by the University - no negative marks – 50 marks.

Note: Both oral and written examinations are mandatory. But separate minimum marks is not insisted for pass. If a students does not complete any of the two assessments, grade I shall be awarded and the final grade shall be given only after the completion of both the assessments.

The two hours allotted for the course may be used by the students for discussion, practice and for oral assessment.

Expected outcome.

- The students will be confident in discussing the fundamental aspects of any engineering problem/situation and give answers in dealing with them